

* NOTICES *

JP0 and INPIT are not responsible for any damages caused by the use of this translation.

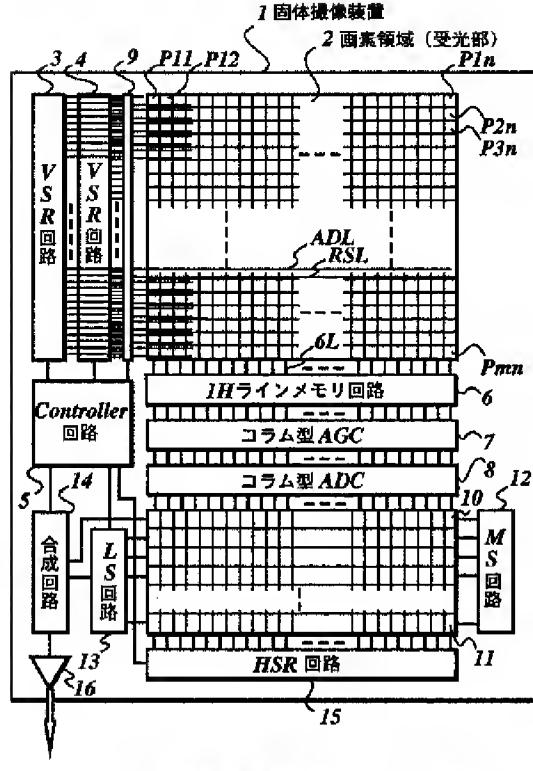
1. This document has been translated by computer. So the translation may not reflect the original precisely.

2. **** shows the word which can not be translated.

3. In the drawings, any words are not translated.

DRAWINGS

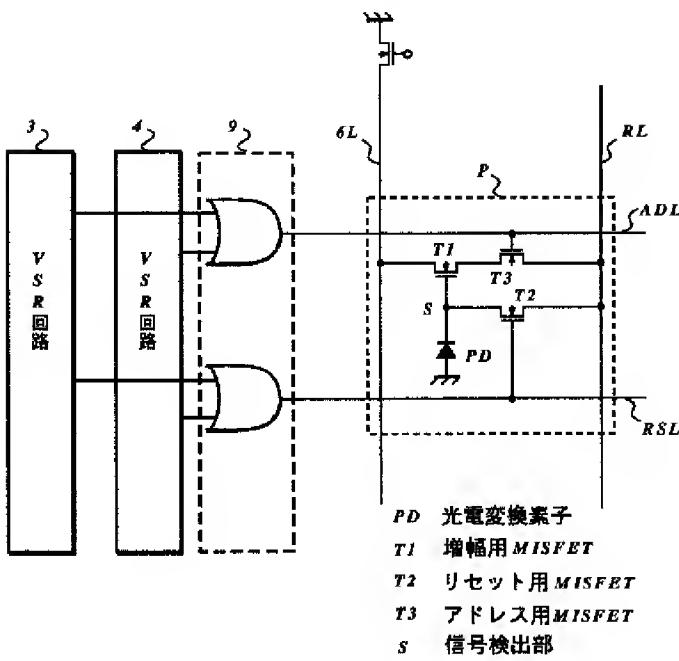
[Drawing 1]



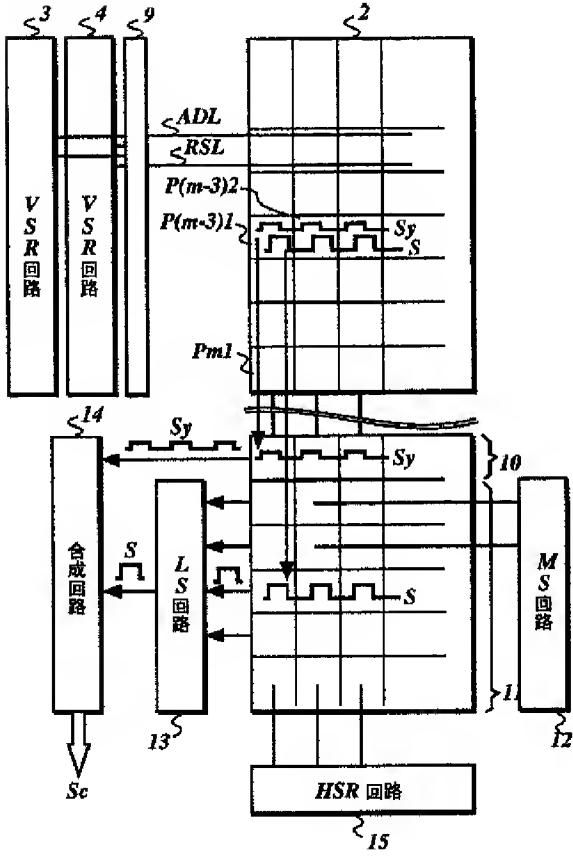
10 高輝度補正信号蓄積メモリ回路

11 画素信号蓄積メモリ回路

[Drawing 2]



[Drawing 3]



[Translation done.]